CP2101

Single-Chip USB to UART Bridge

Single-Chip USB to UART Data Transfer

- Integrated USB Transceiver; No External Resistors Required
- Integrated Clock; No External Crystal Required
- Integrated 512-Byte EEPROM for Vendor ID, Product ID, Serial Number, Power Descriptor, Release Number and Product Description Strings
- On-Chip Power-On Reset Circuit
- On-Chip Voltage Regulator: 3.3 V Output

USB Function Controller

- USB Specification 2.0 Compliant; Full Speed (12 Mbps)
- USB suspend states supported via SUSPEND pins

Asynchronous Serial Data BUS (UART)

- All Handshaking and Modem Interface Signals
- Data Formats Supported:
  - Data Bits: 8
  - Stop Bits: 1
  - Parity: Odd, Even, No Parity
- Baud Rates: 300 bps to 921.6 kbps
- 512 Byte Receive Buffer; 512 Byte Transmit Buffer
- Hardware or X-On / X-Off Handshaking Supported
- Event Character Support

Virtual COM Port Device Drivers

- Works with Existing COM Port PC Applications
- Royalty-Free Distribution License
- Windows 98SE/2000/XP
- MAC OS-9
- MAC OS-X
- Linux 2.40

Example Applications

- Upgrade of RS-232 Legacy Devices to USB
- Cellular Phone USB Interface Cable
- PDA USB Interface Cable
- USB to RS-232 Serial Adapter

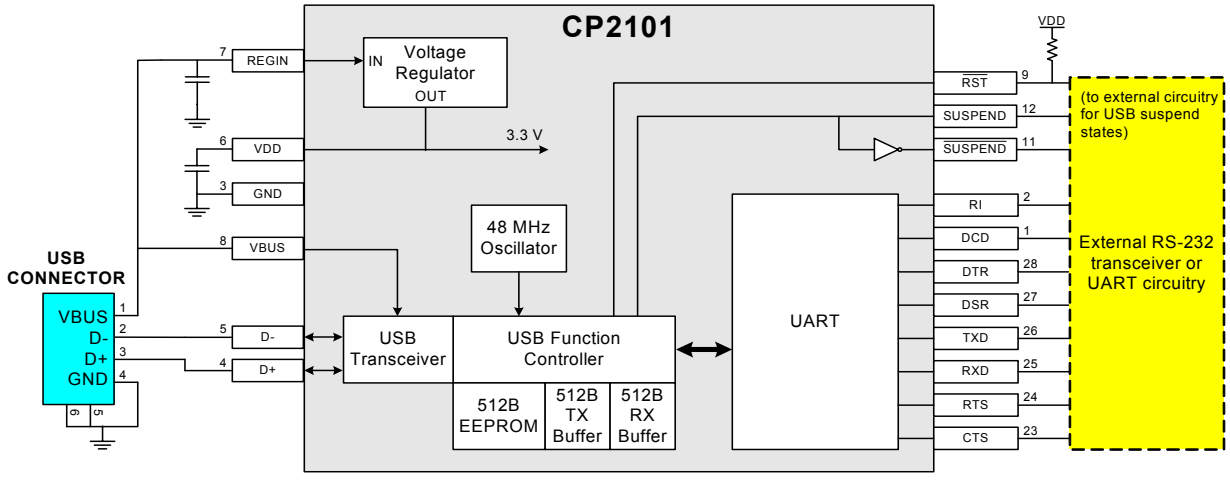
Supply Voltage

- Self-powered: 3.0 to 3.6 V
- USB Bus Powered: 4.0 to 5.25 V

Package

- 28-pin QFN (5 x 5 mm)

Temperature Range: -40 to +85 °C



Example System Diagram

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1. System Overview

The CP2101 is a highly-integrated USB-to-UART Bridge Controller providing a simple solution for updating RS-232 designs to USB using a minimum of components and PCB space. The CP2101 includes a USB 2.0 full-speed function controller, USB transceiver, oscillator, EEPROM and asynchronous serial data bus (UART) with full modem control signals in a compact 5 x 5 mm QFN-28 package. No other external USB components are required.

The on-chip EEPROM may be used to customize the USB Vendor ID, Product ID, Product Description String, Power Descriptor, Device Release Number and Device Serial Number as desired for OEM applications. The EEPROM is programmed on-board via the USB allowing the programming step to be easily integrated into the product manufacturing and testing process.

Royalty-free Virtual COM Port (VCP) device drivers provided by Silicon Laboratories allow a CP2101-based product to appear as a COM port to PC applications. The CP2101 UART interface implements all RS-232 signals, including control and handshaking signals, so existing system firmware does not need to be modified. In many existing RS-232 designs, all that is required to update the design from RS-232 to USB is to replace the RS-232 level-translator with the CP2101.

An evaluation kit for the CP2101 (Part Number: CP2101EK) is available. It includes a CP2101-based USB-to-UART/RS-232 evaluation board, a complete set of VCP device drivers, USB and RS-232 cables, and full documentation. Contact a Silicon Labs' sales representatives or go to www.silabs.com to order the CP2101 Evaluation Kit.

2. Absolute Maximum Ratings

Table 2.1. Absolute Maximum Ratings

Parameter	Conditions	Min	Typ	Max	Units
Ambient temperature under bias		–55	—	125	°C
Storage Temperature		–65	—	150	°C
Voltage on any I/O Pin or $\overline{\text{RST}}$ with respect to GND		–0.3	—	5.8	V
Voltage on V_{DD} with respect to GND		–0.3	—	4.2	V
Maximum Total current through V_{DD} and GND		—	—	500	mA
Maximum output current sunk by $\overline{\text{RST}}$ or any I/O pin		—	—	100	mA

Note: stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the devices at those or any other conditions above those indicated in the operation listings of this specification is not implied. Exposure to maximum rating conditions for extended periods may affect device reliability.

3. Global DC Electrical Characteristics

Table 3.1. Global DC Electrical Characteristics

V_{DD} = 2.7 to 3.6 V, –40 to +85 °C unless otherwise specified

Parameter	Conditions	Min	Typ	Max	Units
Supply Voltage		3.0	3.3	3.6	V
Supply Current	V_{DD} = 3.3 V	—	25	—	mA
Supply Current in Suspend	V_{DD} = 3.3 V	—	325	—	µA
Specified Operating Temperature Range		–40	—	+85	°C

Table 3.2. UART and Suspend I/O DC Electrical Characteristics

V_{DD} = 2.7 to 3.6 V, -40 to +85 °C unless otherwise specified

Parameters	Conditions	Min	Typ	Max	UNITS
Output High Voltage	I_{OH} = -3mA	VDD-0.7			V
	I_{OH} = -10µA	VDD-0.1			
	I_{OH} = -10mA		VDD-0.8		
Output Low Voltage	I_{OL} = 8.5mA			0.6	V
	I_{OL} = 10µA			0.1	
	I_{OL} = 25mA		1.0		
Input High Voltage		2.0			V
Input Low Voltage				0.8	V
Input Leakage Current			25	50	µA

4. Pinout and Package Definitions

Table 4.1. Pin Definitions for the CP2101

Name	Pin #	Type	Description
V_{DD}	6	Power In Power Out	3.0–3.6 V Power Supply Voltage Input. 3.3 V Voltage Regulator Output. See Section 9.
GND	3		Ground
$\overline{RST}$	9	D I/O	Device Reset. Open-drain output of internal POR or V_{DD} monitor. An external source can initiate a system reset by driving this pin low for at least 15 µs.
REGIN	7	Power In	5 V Regulator Input. This pin is the input to the on-chip voltage regulator.
VBUS	8	D In	VBUS Sense Input. This pin should be connected to the VBUS signal of a USB network. A 5 V signal on this pin indicates a USB network connection.
D+	4	D I/O	USB D+
D–	5	D I/O	USB D–
TXD	26	D Out	Asynchronous data output (UART Transmit)
RXD	25	D In	Asynchronous data input (UART Receive)
CTS	23*	D In	Clear To Send control input (active low)
RTS	24*	D Out	Ready to Send control output (active low)
DSR	27*	D in	Data Set Ready control input (active low)
DTR	28*	D Out	Data Terminal Ready control output (active low)
DCD	1*	D In	Data Carrier Detect control input (active low)
RI	2*	D In	Ring Indicator control input (active low)
SUSPEND	12*	D Out	This pin is driven high when the CP2101 enters the USB suspend state.
$\overline{SUSPEND}$	11*	D Out	This pin is driven low when the CP2101 enters the USB suspend state.
NC	10, 13–22		These pins should be left unconnected or tied to V_{DD}.

*Note: Pins can be left unconnected when not used.

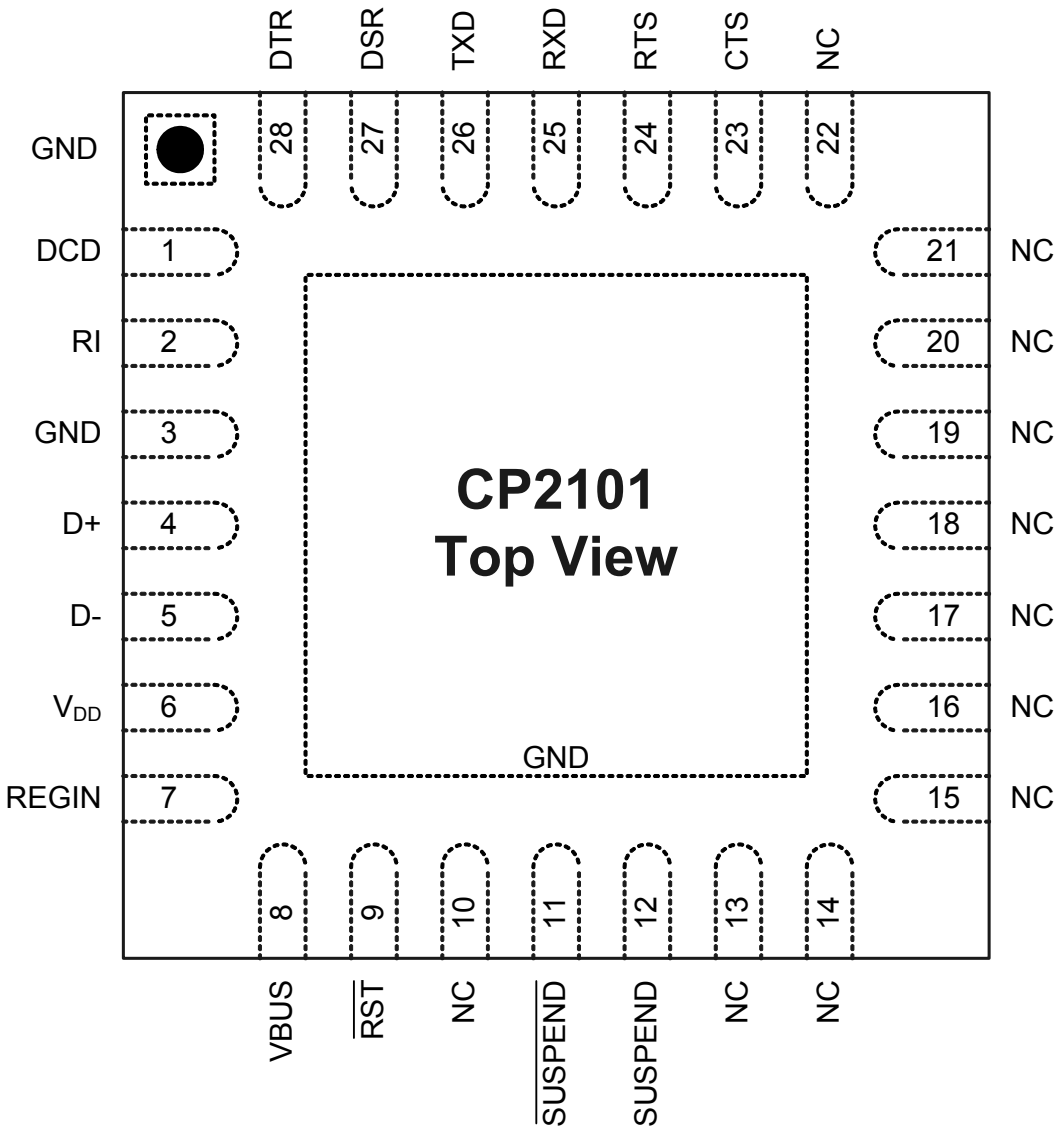


Figure 4.1. QFN-28 Pinout Diagram (Top View)

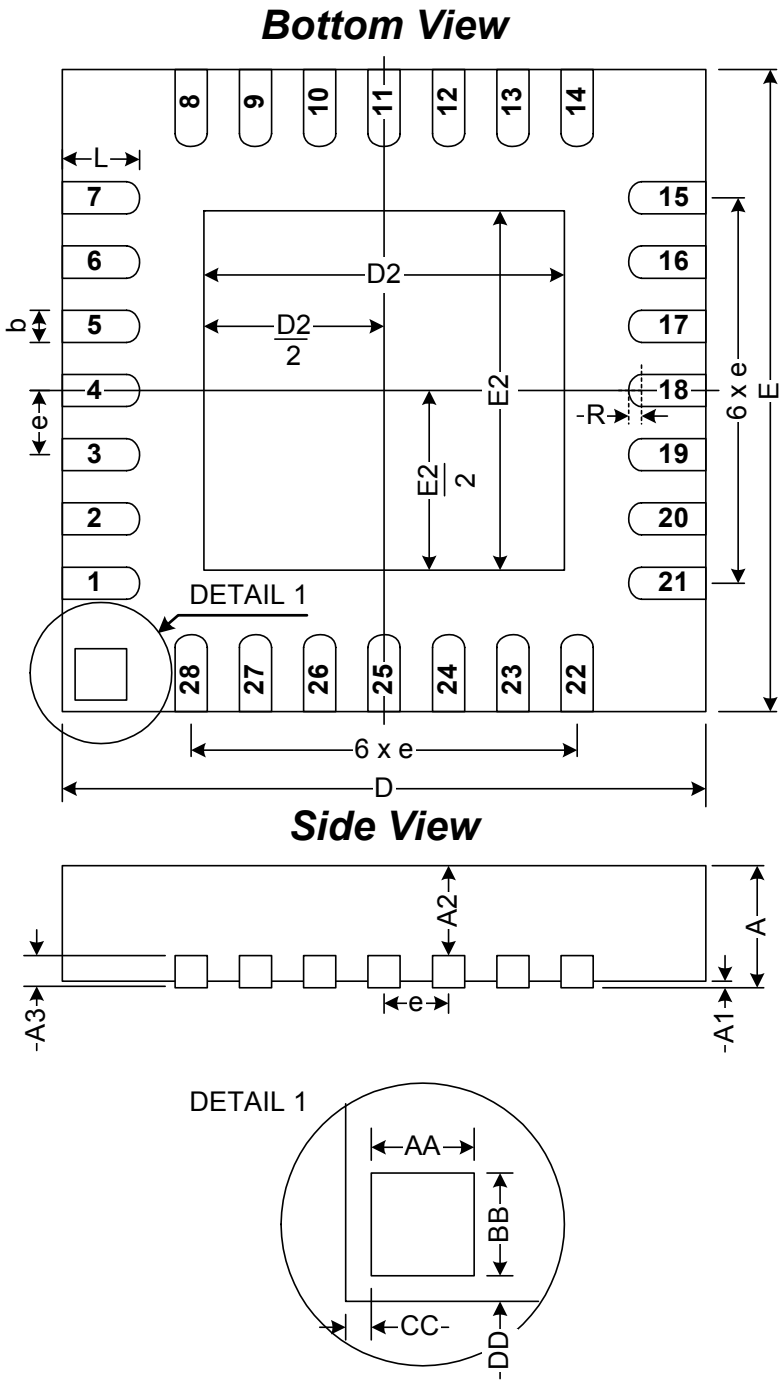


Table 0.1. QFN-28 Package Dimensions

	MM		
	MIN	TYP	MAX
A	0.80	0.90	1.00
A1	0	0.02	0.05
A2	0	0.65	1.00
A3	—	0.25	—
b	0.18	0.23	0.30
D	—	5.00	—
D2	2.90	3.15	3.35
E	—	5.00	—
E2	2.90	3.15	3.35
e	—	0.5	—
L	0.45	0.55	0.65
N	—	28	—
ND	—	7	—
NE	—	7	—
R	0.09	—	—
AA	—	0.435	—
BB	—	0.435	—
CC	—	0.18	—
DD	—	0.18	—

Figure 4.2. QFN-28 Package Drawing

Top View

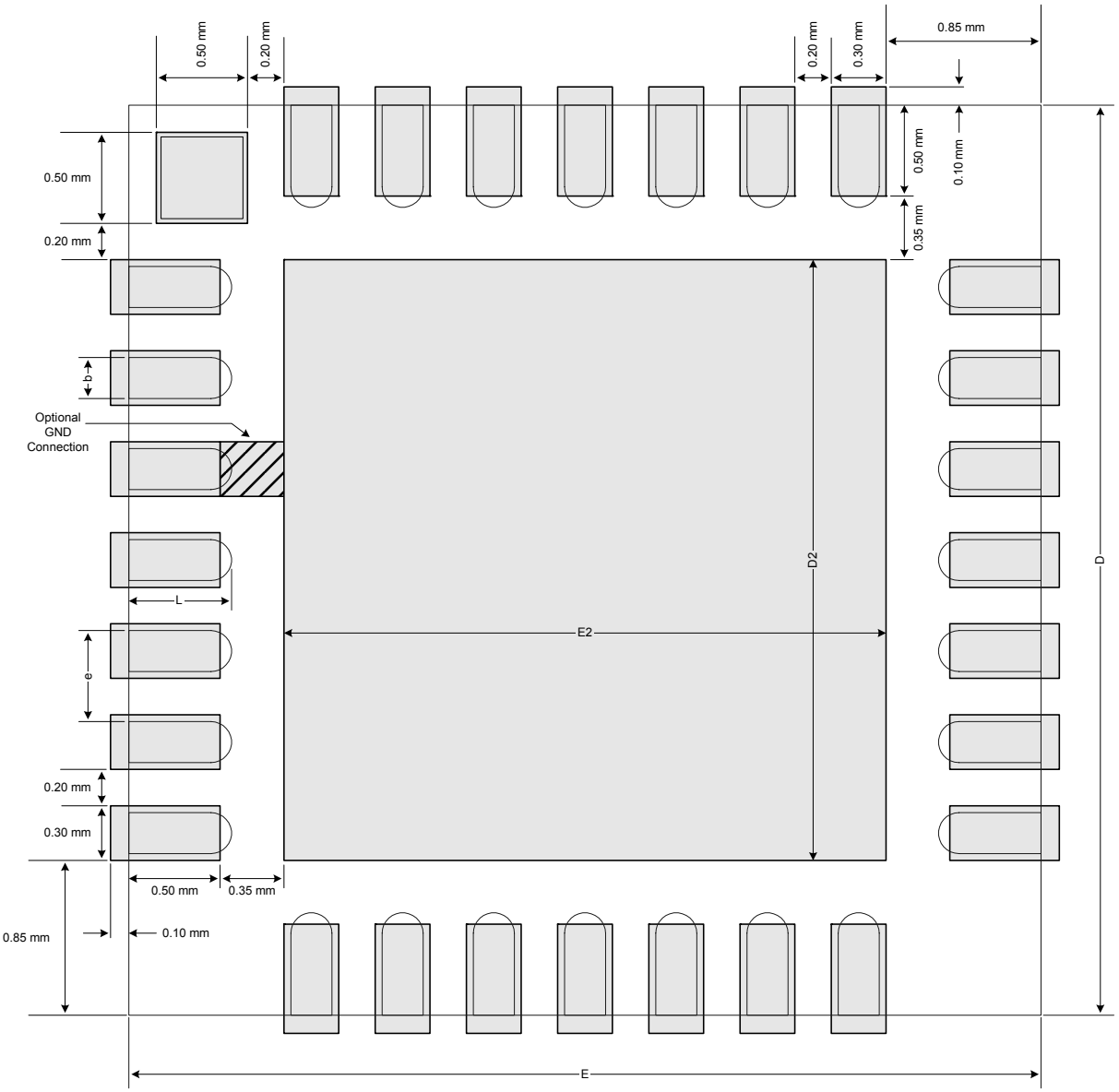


Figure 4.3. Typical QFN-28 Landing Diagram

Top View

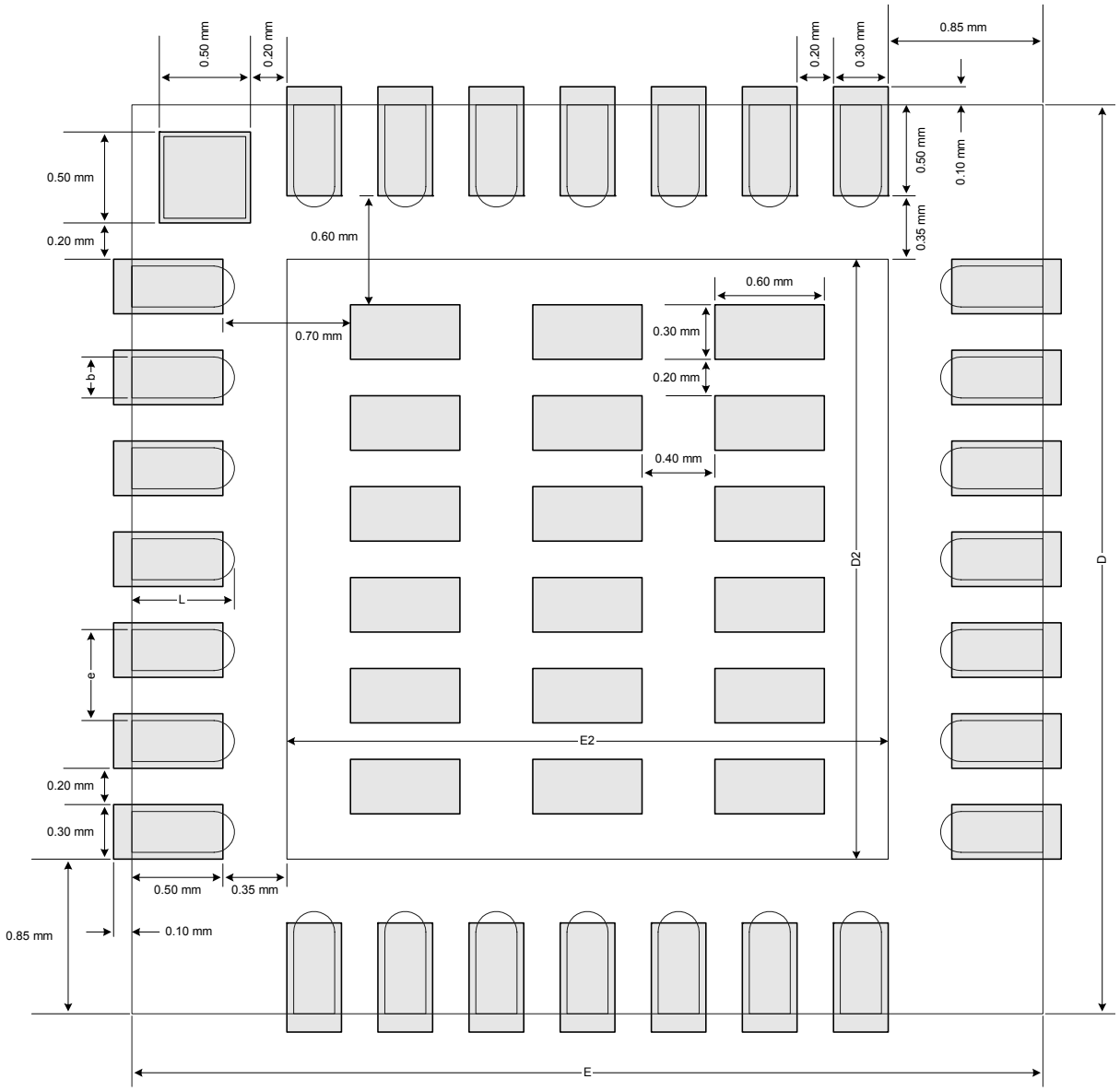


Figure 4.4. Typical QFN-28 Solder Mask

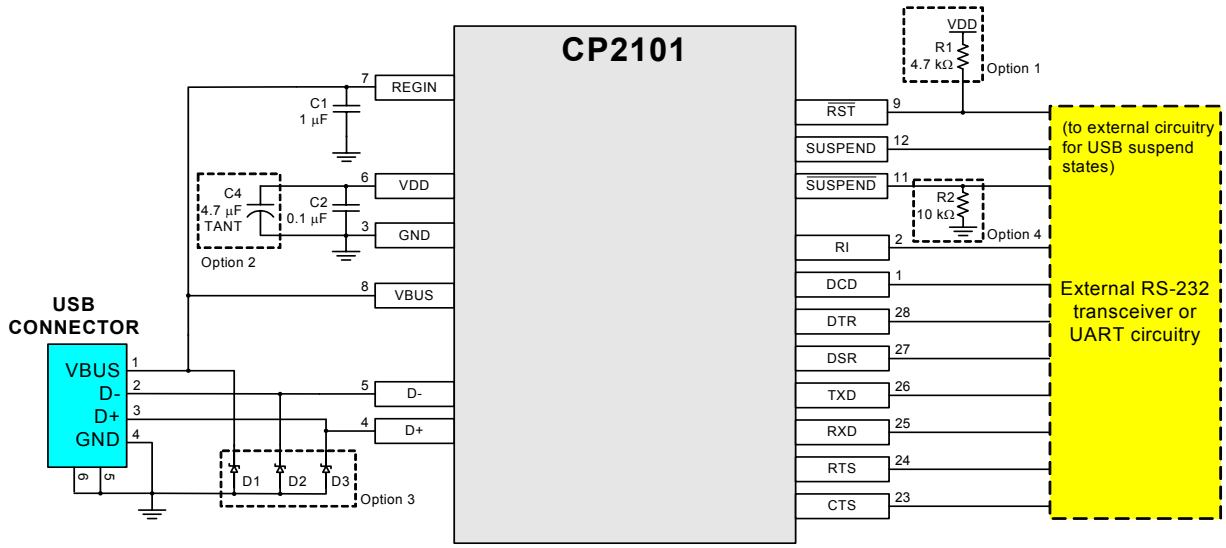
5. USB Function Controller and Transceiver

The Universal Serial Bus function controller in the CP2101 is a USB 2.0 compliant full-speed device with integrated transceiver and on-chip matching and pull-up resistors. The USB function controller manages all data transfers between the USB and the UART as well as command requests generated by the USB host controller and commands for controlling the function of the UART.

The USB Suspend and Resume signals are supported for power management of both the CP2101 device as well as external circuitry. The CP2101 will enter Suspend mode when Suspend signaling is detected on the bus. On entering Suspend mode, the CP2101 asserts the SUSPEND and $\overline{\text{SUSPEND}}$ signals. SUSPEND and $\overline{\text{SUSPEND}}$ are also asserted after a CP2101 reset until device configuration during USB Enumeration is complete

The CP2101 exits the Suspend mode when any of the following occur: (1) Resume signaling is detected or generated, (2) a USB Reset signal is detected, or (3) a device reset occurs. On exit of Suspend mode, the SUSPEND and $\overline{\text{SUSPEND}}$ signals are de-asserted.

Both SUSPEND and $\overline{\text{SUSPEND}}$ temporarily float high during a CP2101 reset. If this behavior is undesirable, a strong pulldown (10 kΩ) can be used to ensure $\overline{\text{SUSPEND}}$ remains low during reset. See Figure 5.1 for other recommended options.



Option 1: A 4.7 kΩ pull-up resistor can be added to increase noise immunity.
Option 2: A 4.7 µF tantalum capacitor can be added if powering other devices from the on-chip regulator.
Option 3: Avalanche transient voltage suppression diodes can be added for ESD protection.
Use Littlefuse p/n SP0503BAHT or equivalent.
Option 4: 10 kΩ resistor to ground to hold $\overline{\text{SUSPEND}}$ low on initial power on or device reset.

Figure 5.1. Typical Connection Diagram

6. Asynchronous Serial Data Bus (UART) Interface

The CP2101 UART interface consists of the TX (transmit) and RX (receive) data signals as well as the RTS, CTS, DSR, DTR, DCD and RI control signals. The UART supports RTS/CTS, DSR/DTR and X-On/X-Off handshaking.

The UART is programmable to support a variety of data formats and baud rates. The data format and baud rate programmed into the UART is set during COM port configuration on the PC. The data formats and baud rates available are listed in Table 6.1.

Table 6.1. Data Formats and Baud Rates

Data Bits	8
Stop Bits	1
Parity Type	None, Even, Odd
Baud Rates	300, 600, 1200, 1800, 2400, 4800, 7200, 9600, 14400, 19200, 28800, 38400, 56000, 57600, 115200, 128000, 230400, 460800, 921600

7. Internal EEPROM

The CP2101 includes an internal EEPROM that may be used to customize the USB Vendor ID, Product ID, Product Description String, Power Descriptor, Device Release Number and Device Serial Number as desired for OEM applications. Customization of the USB configuration data is optional. If the EEPROM is not programmed with OEM data, the default configuration data shown in Table 7.1 is used. However, a unique serial number is required for OEM applications in which it is possible for multiple CP2101-based devices to be connected to the same PC.

The internal EEPROM is programmed via the USB. This allows the OEM's USB configuration data and serial number to be written to the CP2101 on-board during the manufacturing and testing process. A stand-alone utility for programming the internal EEPROM is available from Silicon Laboratories. A library of routines provided in the form of a Windows® DLL is also available. This library can be used to integrate the EEPROM programming step into custom software used by the OEM to streamline testing and serial number management during manufacturing. The EEPROM has a typical endurance of 100,000 write cycles with a data retention of 100 years.

Table 7.1. Default USB Configuration Data

Name	**Value**
Vendor ID	10C4h
Product ID	EA60h
Power Descriptor (Attributes)	80h
Power Descriptor (Max. Power)	32h
Release Number	0100h
Serial Number	0001 (63 characters maximum)
Product Description String	“CP2101 USB to UART Bridge Controller” (126 characters maximum)

8. Virtual Com Port Device Drivers

The CP2101 Virtual COM Port (VCP) device drivers allow a CP2101-based device to appear to the PC's application software as an additional COM port (in addition to any existing hardware COM ports). Application software running on the PC accesses the CP2101-based device as it would access a standard hardware COM port. However, actual data transfer between the PC and the CP2101 device is performed over the USB. Therefore, existing COM port applications may be used to transfer data via the USB to the CP2101-based device without modifying the application. Contact Silicon Laboratories for the latest list of supported operating systems.

Note:
The Silicon Laboratories VCP device drivers are required for device operation and are only distributed as part of the CP2101 Evaluation Kit (Part Number: CP2101EK). Contact any of Silicon Lab's sales representatives or go to www.silabs.com to order the CP2101 Evaluation Kit. The CP2101 drivers and programming utilities are subject to change without notice. Subscription to the website "Auto Email Alert" system for automatic notification of updates and the use of the "Product Update Registration" service is recommended.

9. Voltage Regulator

The CP2101 includes an on-chip 5-to-3 V voltage regulator. This allows the CP2101 to be configured as either a USB bus-powered device or a USB self-powered device. These configurations are shown in Figure 9.1 and Figure 9.2. When enabled, the 3 V voltage regulator output appears on the V_{DD} pin and can be used to power external 3V devices. See Table 9.1 for the voltage regulator electrical characteristics.

Alternatively, if 3 V power is supplied to the V_{DD} pin, the CP2101 can function as a USB self-powered device with the voltage regulator disabled. For this configuration, it is recommended that the REGIN input be tied to the 3 V net to disable the voltage regulator. This configuration is shown in Figure 9.3.

The USB max power and power attributes descriptor must match the device power usage and configuration. See application note “AN144: CP2101 Customization Guide” for information on how to customize USB descriptors for the CP2101.

Note:
It is recommended that additional decoupling capacitance (e.g., 0.1 µF in parallel with 1.0 µF) be provided on the REGIN input.

Table 9.1. Voltage Regulator Electrical Specifications

–40 to +85 °C unless otherwise specified

Parameter	Conditions	Min	Typ	Max	Units
Input Voltage Range		4.0	—	5.25	V
Output Voltage	Output Current = 1 to 100 mA*	3.0	3.3	3.6	V
VBUS Detection Input Threshold		1.0	1.8	4.0	V
Bias Current		—	90	TBD	µA

* The maximum regulator supply current is 100 mA.

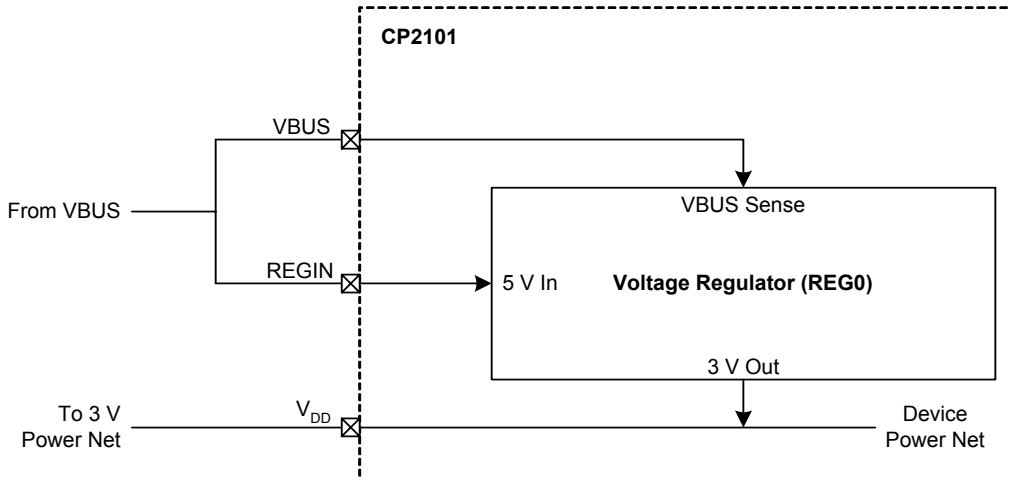


Figure 9.1. Configuration 1: USB Bus-Powered

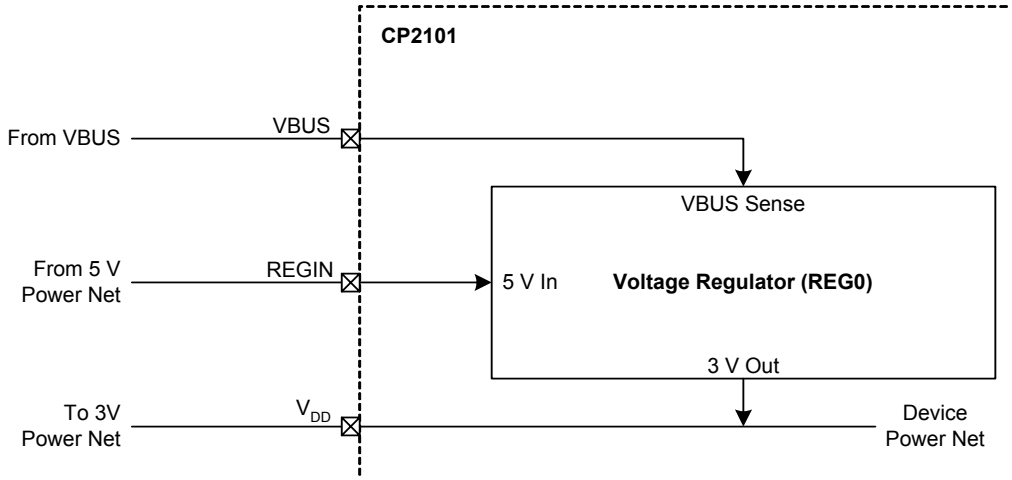


Figure 9.2. Configuration 2: USB Self-Powered

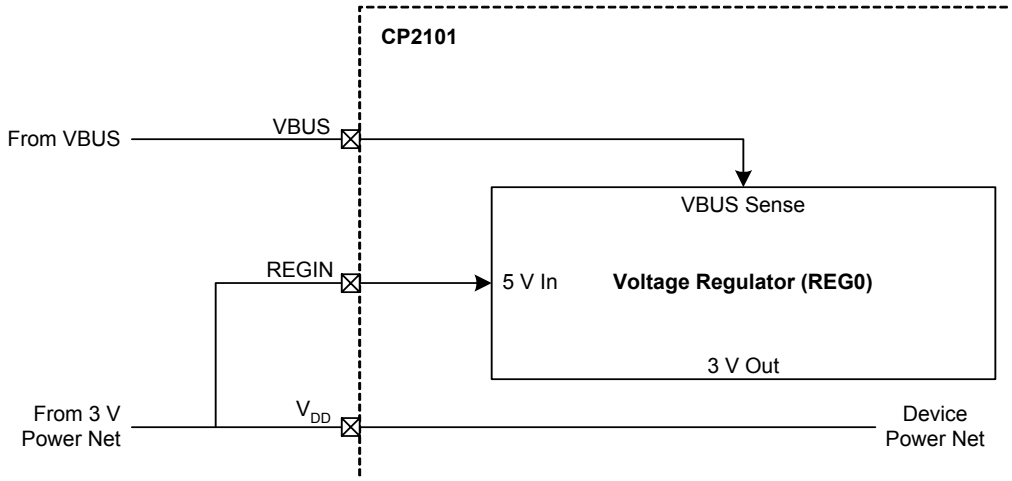


Figure 9.3. Configuration 3: USB Self-Powered, Regulator Bypassed

Document Change List

Revision 1.6 to Revision 1.7

- Updated “Linux 2.40” bullet on page 1.
- Changed MLP to QFN throughout.

Revision 1.7 to Revision 1.71

- Removed “Windows CE” bullet from page 1.

Notes:

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